## Amendments to claims:

This listing of claims will replace all prior versions and listings of claims in the application:

## **Listing of Claims:**

Claim 1 (Currently amended) A driving circuit for a flat display panel, comprising:

a plurality of video signal lines for providing video signals;

at least one buffer unit for inverting a scanning signal; and

a plurality of switch units disposed between said plurality of video signal lines;

wherein each of said plurality of switch units is connected to at least one video signal line to receive a video signal and is connected to said buffer unit to receive

said scanning signal inverted by said buffer unit, and

whereby wherein said scanning signal controls the operation of output of said video signals by the plurality of switch units and a video signal is outputted to a display area of said flat display panel.

Claim 2 (Previously Presented) The driving circuit of claim 1, wherein said plurality of switch units and said display area of said flat display panel are spaced apart with at least one video signal line.

Claim 3 (Original) The driving circuit of claim 1, wherein said buffer unit for inverting a scanning signal is an inverting circuit receiving a timing signal which is then inverted to output at least one scanning signal.

Claim 4 (Original) The driving circuit of claim 2, wherein said at least one scanning signal is an inversed signal of said timing signal.

Claim 5 (Previously Presented) The driving circuit of claim 1, wherein said plurality of switch units comprise thin-film transistors.

Claim 6 (Previously Presented) The driving circuit of claim 1, wherein said at least one video signal line is disposed between said plurality of switch units and

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said buffer unit for inverting a scanning signal.

Claim 7 (Previously Presented) The driving circuit of claim 1, wherein said plurality of video signal lines are disposed between said switch units and said display area.

Claim 8 (Previously Presented) The driving circuit of claim 1, wherein said flat display panel comprises a liquid crystal display panel.